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**CUSTOMER NO.: 84274**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of : Jozef Laurentius Kessels  
Application No. : 10/591,546  
Filed : September 1, 2006  
For : CIRCUIT COMPRISING MUTUALLY ASYNCHRONOUS  
CIRCUIT MODULES  
Art Unit : 2115  
Examiner : Abbaszadeh, Jaweed A.  
Confirmation No. : 4840

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT**

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The reference herein is listed below and on the attached Form PTO/SB/08A.

**U.S. Pat./Pub. No.**

**Inventor**

**Date**

2002/0181631

Mackey, et al.

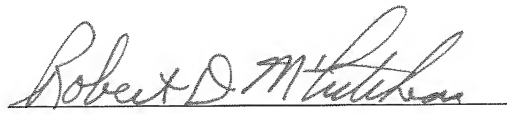
Dec. 5, 2002

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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Date: 8/25/2009

  
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